



Replacement Sheets

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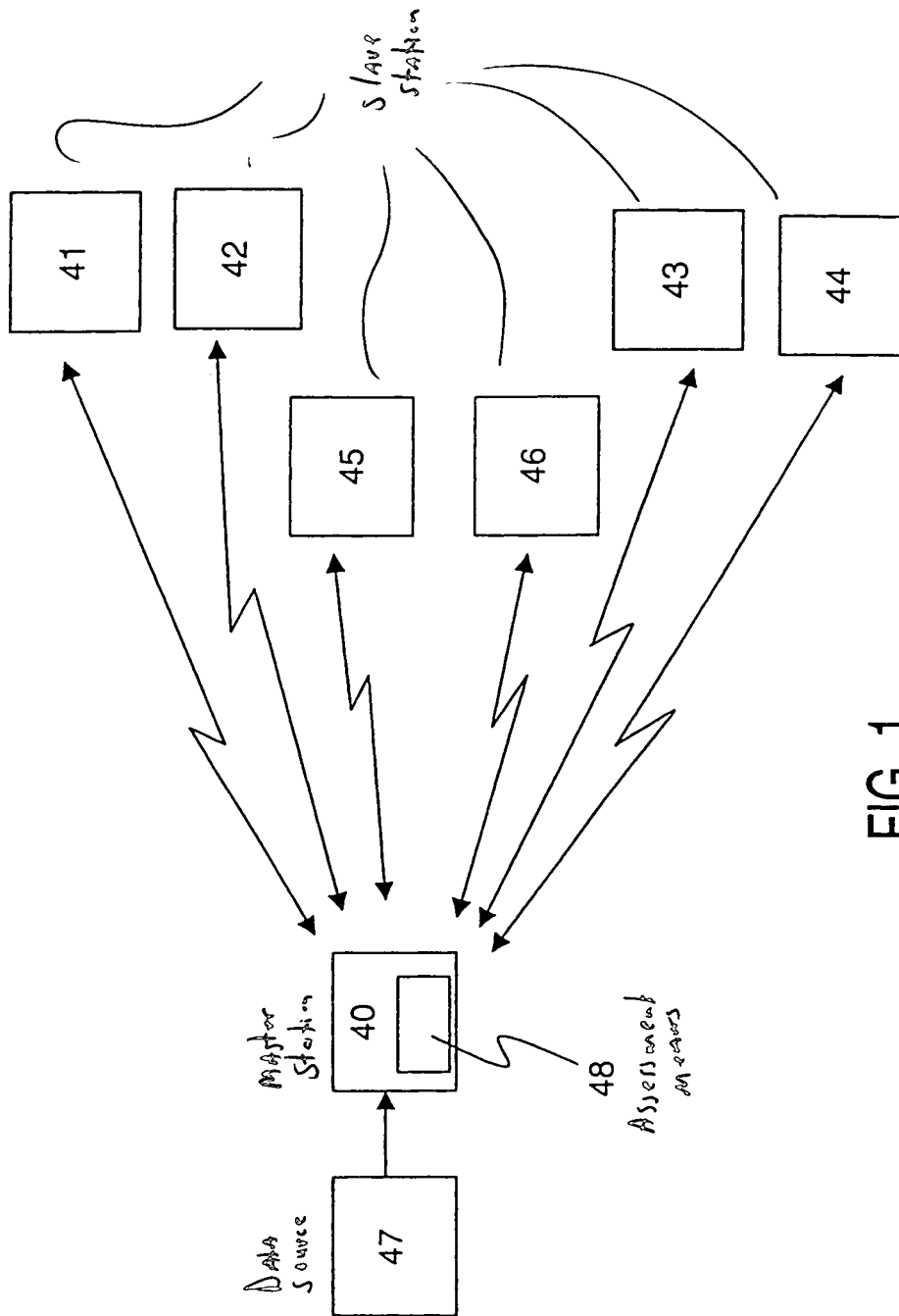


FIG. 1

The diagram illustrates a 64-bit bus system divided into four 16-bit segments. The bus is shown in three phases: Assessment phase, Assignment phase, and Acknowledged phase. The bus is divided into four 16-bit segments. In the Assessment phase, segments 1-4 contain values 21, 22, 23, 24. In the Assignment phase, segments 1-4 contain values 33, 35, 33, 34. In the Acknowledged phase, segments 1-4 contain values 51, 52, 53, 54. Arrows indicate data flow between segments and between phases.

Segment	Assessment phase	Assignment phase	Acknowledged phase
1	21	33	51
2	22	35	52
3	23	33	53
4	24	34	54

FIG. 2

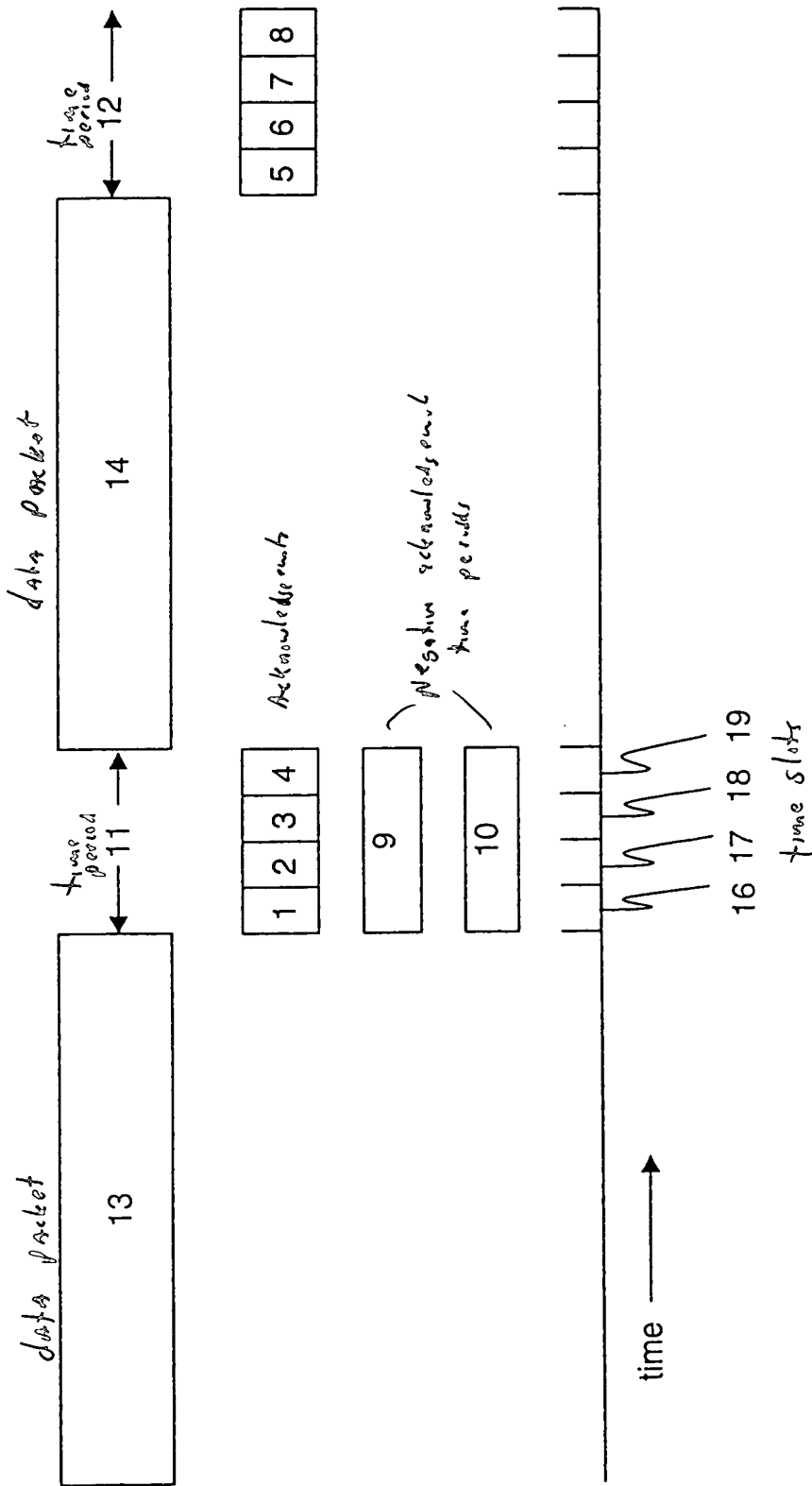


FIG. 3